

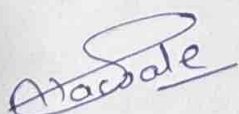
Progressive Education Society's
Modern College of Arts, Science and Commerce (Autonomous),
Shivajinagar, Pune 5

Department of Computer Science
F.Y.B. Sc. (Computer Science) Div A
Timetable 2026-27
Semester I

Classroom: CC5

Time	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
7:15 AM To 11:15 AM	Electronics Practical	Electronics Practical	Electronics Practical	Electronics Practical	Electronics Practical	Electronics Practical
8:00 AM To 10:00 AM	Computer Science Practical	Computer Science Practical	Computer Science Practical	Computer Science Practical	Computer Science Practical	Computer Science Practical
11:15 AM To 12:15 PM (Theory)	O.E.	O.E.	—	Discrete Mathematics (Maths)	Statistical Analysis using R Programming (Statistics)	—
12:15 PM To 1:15 PM (Theory)	Basics of Electronics Components and Circuits (Electronics)	Programming in 'C' (Comp. Sci.)	Statistical Analysis using R Programming (Statistics)	Programming in 'C' (Comp. Sci.)	Basics of Electronics Components and Circuits (Electronics)	Discrete Mathematics (Maths)
1:30 PM To 5:30 PM	Mathematics Practical	Mathematics Practical	Mathematics Practical	Mathematics Practical	Mathematics Practical	Mathematics Practical
2:00 PM To 4:00 PM	Computer Science Practical	Computer Science Practical	Computer Science Practical	Computer Science Practical	Computer Science Practical	Computer Science Practical
2:00 PM To 6:00 PM	Statistics Practical	Statistics Practical	Statistics Practical	Statistics Practical	Statistics Practical	Statistics Practical

Note: Practical will be conducted as per the allotted batches.


Coordinator,
B. Sc. Computer Science




Prof. S. S. Deshmukh
Vice Principal
Head, Dept. of Computer Science