

*Progressive Education Society's*  
**Modern College of Arts, Science and Commerce (Autonomous), Shivajinagar, Pune 5.**


**ESE (End Semester Examination) October-November 2025**


**M.Sc. ELECTRONIC SCIENCE (SEM-IV) Backlog**


**TIME-TABLE**

Course Type	Course Code	Course	Course / Paper Title	Credit	ESE MARKS	DATE	TIME
Extra Credit	23Cpskdp402	Extra Credit	Skill Development-II	2	50	At Department	
Extra Credit	23CpHrtP201	Extra Credit	Human Right-II	1	25	16/12/2025	2.30 pm-3.00 pm
Extra Credit	23CpcysP401	Extra Credit	Cyber Security-II	2	50	02/12/2025	2.30 pm-3.30 pm
Major Mandatory (4 + 4+4+2)	23ScEleP411	Major Paper 1 (Theory)	Industry 4.0	4	50	03/12/2025	2.30 pm-4.30 pm
	23ScEleP412	Major Paper 2 (Theory)	Process Control Systems	4	50	04/12/2025	2.30 pm-4.30 pm
	23ScEleP413	Major Paper 3 (Theory)	E waste management	4	50	05/12/2025	2.30 pm-4.30 pm
Major Electives (4)	23ScEleP421	Major Elective 1 (Theory)	Power Electronics & Renewable Energy Systems	4	50	06/12/2025	2.30 pm-4.30 pm
	23ScEleP422	Major Elective 2 (Theory)	Automotive Electronics	4	50	06/12/2025	2.30 pm-4.30 pm
RP (4)	23ScEleP451	RP	Research Project II	6	75	At Department	

  
**Dr. Yugandhar Shinde**  
Dy. COE

  
**Dr. R. M. Jagtap**  
COE

  
**Dr. Anjali Sardesai**  
V.P. (Academics)

  
**Dr. Nivedita Ekbote**  
Principal

**Instructions to Students:**

1. Please ensure that you will reach the examination Hall 25 minutes before examination time.
2. Duly signed Hall ticket and college ID card is compulsory to attend the examinations.
3. Use of mobile phone and smart watch is strictly prohibited.
4. If any misconduct by any student will be confirmed by Examination Squad during examination, then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.
5. For CIE Backlogs, contact the concerned Department.