


Progressive Education Society's
Modern College of Arts, Science and Commerce (Autonomous), Shivajinagar, Pune 5.
ESE (End Semester Examination) October-November 2025
M.Sc. ELECTRONIC SCIENCE (SEM-II) Backlog

TIME-TABLE

Course Type	Course Code	Course	Course / Paper Title	Credit	ESE MARKS	DATE	TIME
Extra Credit	23CpHrtP201	Extra Credit	Human Right-II	1	25	16/12/2025	2.30pm-3.00pm
Major Mandatory (4 + 4+4+2)	23ScEleP211	Major Paper 1 (Theory)	Advanced Microcontrollers and its Applications	4	50	17/12/2025	2.30pm-4.30pm
	23ScEleP212	Major Paper 2 (Theory)	Electromagnetic Fields and Antennas	4	50	18/12/2025	2.30pm-4.30pm
	23ScEleP213	Major Paper 4 (Theory)	Advanced Electronic communication Systems	4	50	At Department	
	23ScEleP214	Major Paper 4 (Theory)	Advanced Electronic communication Systems	2	25	20/12/2025	2.30pm-3.30pm
Major Electives (4)	23ScEleP221	Major Elective 1 (Theory)	Solid State Devices	4	50	22/12/2025	2.30pm-4.30pm
	23ScEleP222	Major Elective 2 (Theory)	Nanoelectronics	4	50	22/12/2025	2.30pm-4.30pm
OJT (4)	23ScEleP241	OJT	On Job Training	4	50	At Department	


Dr. Yugandhar Shinde
Dy. COE


Dr. R. M. Jagtap
COE


Dr. Anjali Sardesai
V.P. (Academics)


Dr. Nivedita Ekbote
Principal

Instructions to Students:

1. Please ensure that you will reach the examination Hall 25 minutes before examination time.
2. Duly signed Hall ticket and college ID card is compulsory to attend the examinations.
3. Use of mobile phone and smart watch is strictly prohibited.
4. If any misconduct by any student will be confirmed by Examination Squad during examination, then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.

5. For CIE Backlogs, contact the concerned Department.