

Progressive Education Society's

Only for Autonomous As prepared on: 20-09-2025

Modern College of Arts, Science and Commerce (Autonomous)

Shivajinagar, Pune -5

Time Table for Second Year PG SCIENCE FACULTY (M.Sc. Electronic Sci

ESE Examination OCT-NOV2025 Sem – III Backlog

Date	Time	Subject Code	Subject Name
Extra credit			
12-12-2025	2.30 pm to 3.30pm	20CpPolP401	Introduction to Constitution
13-12-2025	2.30 pm to 3.30pm	19CpCysP301	Cyber Security III
Regular credit			
15-12-2025	2.30 pm to 4.30 pm	19ScEleP301	PIC Microcontroller
16-12-2025	2.30 pm to 4.30 pm	19ScEleP304/	ARM and RTOS/
		19ScEleP305	Digital Signal Processing
17-12-2025	2.30 pm to 4.30 pm	19ScEleP306/	Digital Image and Video Processing/
		19ScEleP307	Electronics and Computer Architecture









Dr. Yugandhar Shinde Dy. COE

Dr. R. M. Jagtap COE

Dr. Anjali Sardesai V.P. (Academics)

Dr. Nivedita Ekbote Principal

Instructions to Students:

- 1. Please ensure that you will reach the examination Hall 25 minutes before examination time.
- 2. Duly signed Hall ticket and college ID card is compulsory to attend the examinations
- 3. Use of mobile phone and smart watch is strictly prohibited.
- **4.** If any misconduct by any student will be confirmed by Examination Squad during examination then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.
- **5.** For CIE Backlogs, contact the concerned Department.