



Progressive Education Society's
Modern College of Arts, Science and Commerce (Autonomous)
Shivajinagar, Pune -5
Time Table for Third Year UG
SCIENCE FACULTY
(T.Y.B.SC. ELECTRONIC SCIENCE) SEM V
CIE Regular Examination, February 2025
(Sem-VI Regular)

Date	Time	Subject Code	Subject
04-02-2025	12.00 noon to 12.40 pm	19ScEleU601	Electronic Instrumentation and Robotics
	12.45 pm to 1.25 pm	19ScEleU602	Advanced Microcontroller: PIC
05-02-2025	12.00 noon to 12.40 pm	19ScEleU603	Industrial Power Electronics
	12.45 pm to 1.25 pm	19ScEleU604	Modeling and Simulation of System using MATLAB
06-02-2025	12.00 noon to 12.40 pm	19ScEleU605	Advanced Electronic Communication and IoT
	12.45 pm to 1.25 pm	19ScEleU607	Fiber Optic Communication

Dr. R. M. Jagtap
Controller of Examinations

Dr. Anjali Sardesai
Coordinator, IQAC

Dr. R. S. Zunjarrao
Principal

Instructions:

1. Reach the examination hall strictly 15 minutes before examination time.
2. The Regular CIE examinations will be conducted in offline mode at college.
3. The seating arrangement will be provided by concerned Department one day before examination date.
4. Use of mobile phone and smart watch is strictly prohibited.
5. Use of calculator can be done with prior permission of concern Subject Teacher.
6. Signing the Attendance sheet of the examination is compulsory.
7. MOOC based extra credits examinations will be conducted along with the ESE examinations.