



Only for Autonomous
As prepared on: 11/10/2024

Progressive Education Society's
Modern College of Arts, Science and Commerce (Autonomous)
Shivajinagar, Pune -5
Time Table for First Year PG
SCIENCE FACULTY M.Sc. Electronic Science
ESE Examination Oct-Nov 2024
Sem – I Backlog

Date	Time	Paper Code	Subject
Extra Credits			
22/11/2024	2.30 pm to 3.30 pm	19CpHrtP102	Human Right-I
23/11/2024	2.30 pm to 3.30 pm	19CpCysP101	Cyber Security-I
Regular Credits			
25/11/2024	2.30 pm to 4.30 pm	19ScEleP101	Analog Circuit Design and Analysis
27/11/2024	2.30 pm to 4.30 pm	19ScEleP102	Mathematical Methods and Network analysis.
28/11/2024	2.30 pm to 4.30 pm	19ScEleP103	Foundation of Semiconductor Devices and Circuits
29/11/2024	2.30 pm to 4.30 pm	19ScEleP104	Advanced 'C' Programming

Dr. Yugandhar Shinde
Dy. COE

Dr. R. M. Jagtap
CoE

Dr. Anjali Sardesai
Coordinator, IQAC

Dr. R. S. Zunjarrao
Principal

Instructions to Students:

1. Please ensure that you will reach the examination Hall 25 minutes before examination time.
2. Duly signed Hall ticket and college ID card is compulsory to attend the examinations
3. Use of mobile phone and smart watch is strictly prohibited.
4. If any misconduct by any student will be confirmed by Examination Squad during examination then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.
5. For CIE Backlogs, contact the concerned Department.