Progressive Education Society's
Modern College of Arts, Science and Commerce
(*Autonomous*)
Shivajinagar, Pune -5
Time Table for Second Year PG
SCIENCE FACULTY (M.Sc. Electronic Science)
ESE Examination Mar-Apr 2024
Sem – III Backlog

Date	Time	Subject Code	Subject Name
Extra credit			
29-04-2024	2.30 pm to 3.30pm	20CpPolP401	Introduction to Constitution
30-04-2024	2.30 pm to 3.30pm	19CpCysP301	Cyber Security III
Regular credit			
02-05-2024	2.30 pm to 4.30 pm	19ScEleP301	PIC Microcontroller
03-05-2024	2.30 pm to 4.30 pm	19ScEleP304/ 19ScEleP305	ARM and RTOS/ Digital Signal Processing
04-05-2024	2.30 pm to 4.30 pm	19ScEleP306/ 19ScEleP307	Digital Image and Video Processing/ Electronics and Computer Architecture

Dr. Yugandhar Shinde
Dy. COE

Dr. R. M. Jagtap
CoE

Dr. Anjali Sardesai
Coordinator, IQAC

Dr. R. S. Zunjarrao
Principal

Instructions to Students:
1. Please ensure that you will reach the examination Hall 25 minutes before examination time.
2. Duly signed Hall ticket and college ID card is compulsory to attend the examinations
3. Use of mobile phone and smart watch is strictly prohibited.
4. If any misconduct by any student will be confirmed by Examination Squad during examination then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.
5. For CIE Backlogs, contact the concerned Department.