Only for Autonomous As prepared on: 24-01-2024



Progressive Education Society's Modern College of Arts, Science and Commerce (Autonomous) Shivajinagar, Pune -5 Time Table for Third Year UG

SCIENCE FACULTY (T.Y.B.SC. ELECTRONIC SCIENCE) SEM VI

CIE Regular Sem VI February 2024

| Date | Time | Subject Code | Subject |
|------------|----------------------|--------------|--|
| | 12.00 pm to 12.30 pm | 19ScEleU601 | Electronic Instrumentation and Robotics |
| 05/02/2024 | 12.45 pm to 1.15 pm | 19ScEleU602 | Advanced Microcontroller : PIC |
| 06/02/2024 | 12.00 pm to 12.30 pm | 19ScEleU603 | Industrial Power Electronics |
| | 12.45 pm to 1.15 pm | 19ScEleU604 | Modeling and Simulation of System using MATLAB |
| 07/02/2024 | 12.00 pm to 12.30 pm | 19ScEleU605 | Advanced Electronic Communication and IoT |
| | 12.45 pm to 1.15 pm | 19ScEleU607 | Fiber Optic Communication |

Dr. R. M. Jagtap Controller of Examinations Dr. Anjali Sardesai Coordinator, IQAC

Dr. R. S. Zunjarrao Principal

Instructions to Students:

- 1. Reach the examination hall strictly 15 minutes before examination time.
- 2. The Regular CIE examinations will be conducted in offline mode at college.
- 3. The seating arrangement will be provided by concerned Department one day before examination date.
- 4. Use of mobile phone and smart watch is strictly prohibited.
- 5. Use of calculator can be done with prior permission of concern Subject Teacher.
- 6. Signing the Attendance sheet of the examination is compulsory.
- 7. MOOC based extra credits examinations will be conducted along with the ESE examinations.
- 8. If SY/TY UG 2024-24 Provisionally admitted students will not earn the required credit pre-requisite to confirm their 2024-24 SY/TY UG admission then they should not attempt this CIE examination. If such Fail students will attempt Feb-2024 CIE regular examinations, their marks will not be considered for any result calculations.