



Only for Autonomous
As prepared on: 19-08-2024

Progressive Education Society's
Modern College of Arts, Science and Commerce (Autonomous)
Shivajinagar, Pune -5
Time Table for Third Year UG
(T.Y.B.SC. ELECTRONIC SCIENCE) SEM V
CIE Examination, September 2024
Sem V Regular

Date	Time	Subject Code	Subject
02-09-2024	12.30 pm to 1.00 pm	19ScEleU501	Analog Integrated Circuits and applications
	1.15 pm to 1.45 pm	19ScEleU502	8051 Microcontroller interfacing Applications
03-09-2024	12.30 pm to 1.00 pm	19ScEleU503	Digital System Design with verilog
	1.15 pm to 1.45 pm	19ScEleU504	Principles of Semiconductor Dev
04-09-2024	12.30 pm to 1.00 pm	19ScEleU505	Fundamentals of Electronic Communication
	1.15 pm to 1.45 pm	19ScEleU506	"C" Programming

Dr. R. M. Jagtap
Controller of Examinations

Dr. Anjali Sardesai
Coordinator, IQAC

Dr. R. S. Zunjarrao
Principal

Instructions:

1. Reach the examination hall strictly 15 minutes before examination time.
2. The Regular CIE examinations will be conducted in offline mode at college.
3. The seating arrangement will be provided by concerned Department one day before examination date.
4. Use of mobile phone and smart watch is strictly prohibited.
5. Use of calculator can be done with prior permission of concern Subject Teacher.
6. Signing the Attendance sheet of the examination is compulsory.
7. MOOC based extra credits examinations will be conducted along with the ESE examinations.