



Progressive Education Society's
Modern College of Arts, Science and Commerce (Autonomous)

Shivajinagar, Pune -5
Time Table for First Year UG

Science Faculty
S.Y. B. SC COMP. SCIENCE SEM-IV
CIE Backlog Examination, Mar-Apr 2024
Sem IV Backlog

Only for Autonomous
As prepared on: 05-03-2024

Date	Time	Subject Code	Subject
Regular Credits			
27-03-2024	4.45 pm to 5.30 pm	19CpEngU402	English Communication –II
28-03-2024	4.45 pm to 5.30 pm	19CsCmpU401	Advanced Data Structures
30-03-2024	4.45 pm to 5.30 pm	19CsCmpU402	Object Oriented Programming using C++
01-04-2024	4.45 pm to 5.30 pm	19CsEleU401	System Designing with Raspberry Pi
02-04-2024	4.45 pm to 5.30 pm	19CsEleU402	Advanced Communication Technologies
03-04-2024	4.45 pm to 5.30 pm	19CsMatU401	Computational Geometry
04-04-2024	4.45 pm to 5.30 pm	19CsMatU402	Operations Research

Dr. Yugandhar Shinde
Dy. COE

Dr. R. M. Jagtap
CoE

Dr. Anjali Sardesai
Coordinator, IQAC

Dr. R. S. Zunjarrao
Principal

Instructions to Students:

1. The examination hall for CIE Backlog Examination will be communicated to you by concerned Department.
2. For any query related to CIE Backlog Examination time-table, please contact concerned subject teacher /Department.
3. For CIE Backlog Examination, please ensure that you will reach the examination Hall 10 minutes before examination time.
4. Duly signed Hall ticket and college ID card is compulsory to attend the examinations.
5. Use of mobile phone and smart watch is strictly prohibited.
6. If any misconduct by any student will be confirmed by Examination Squad during examination, then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.