Only for Autonomous As prepared on 05-03-2024



Progressive Education Society's

Modern College of Arts, Science and Commerce (Autonomous)

Shivajinagar, Pune -5 Time Table for Third Year UG

SCIENCE

(T.Y.BCA SCIENCE) SEM VI

CIE Backlog Examination, Mar-Apr 2024 (Sem-VI - Backlog)

FACULTY

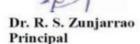
Date	Time	Subject Code	Subject
01-04-2024	1.15 pm to 2.00 pm	19ScBCAU601	Programming in Advanced JAVA
02-04-2024	1.15 pm to 2.00 pm	19ScBCAU602	Mobile Technologies (Android Development)
03-04-2024	1.15 pm to 2.00 pm	19ScBCAU603	Internet of Things (IoT)
04-04-2024	1.15 pm to 2.00 pm	19ScBCAU604	Cloud and Grid Computing
05-04-2024	1.15 pm to 2.00 pm	19ScBCAU605/ 19ScBCAU606	Software Project Management / Research Methodology



Dr. R. M. Jagtap Controller of Examinations



Dr. Anjali Sardesai Coordinator, IQAC



Instructions to Students:

- 1. The examination hall for CIE Backlog Examination will be communicated to you by concerned Department.
- 2. For any query related to CIE Backlog Examination time-table, please contact concerned subject teacher /Department.
- **3.** For CIE Backlog Examination, please ensure that you will reach the examination Hall 10 minutes before examination time.
- **4.** Duly signed Hall ticket and college ID card is compulsory to attend the examinations.
- 5. Use of mobile phone and smart watch is strictly prohibited.
- **6.** If any misconduct by any student will be confirmed by Examination Squad during examination then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.