

## Progressive Education Society's Modern College of Arts, Science and Commerce (Autonomous) Shivajinagar, Pune -5 Time Table for First Year UG

Science Faculty
S.Y. BCA SCI SEM-III

## CIE Backlog Examination, Mar-Apr 2024 Sem III Backlog

Date	Time	Subject Code	Subject
Regular Credits			
02 04 2024	1.45 1- 5.20	10C F 11202	Earlish Communication I
03-04-2024	4.45 pm to 5.30 pm	19CpEngU302	English Communication –I
04-04-2024	4.45 pm to 5.30 pm	19ScBCAU301	Data Structure
05-04-2024	4.45 pm to 5.30 pm	19ScBCAU302	Database System-II
06-04-2024	4.45 pm to 5.30 pm	19ScBCAU303	Software Engineering Practices
08-04-2024	4.45 pm to 5.30 pm	19ScBCAU304	Operating System Concepts

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Dr. Yugandhar Shinde Dy. COE W/7.

Dr. R. M. Jagtap CoE Dr. Anjali Sardesai Coordinator, IQAC

Dr. R. S. Zunjarrao Principal

## **Instructions to Students:**

- 1. The examination hall for CIE Backlog Examination will be communicated to you by concerned Department.
- 2. For any query related to CIE Backlog Examination time-table, please contact concerned subject teacher /Department.
- 3. For CIE Backlog Examination, please ensure that you will reach the examination Hall 10 minutes before

examination time.

- 4. Duly signed Hall ticket and college ID card is compulsory to attend the examinations.
- 5. Use of mobile phone and smart watch is strictly prohibited.
- 6. If any misconduct by any student will be confirmed by Examination Squad during examination, then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.