



Only for Autonomous
As prepared on: 05-03-2024

Progressive Education Society's
Modern College of Arts, Science and Commerce (Autonomous)
Shivajinagar, Pune -5
Time Table for Third Year UG
SCIENCE FACULTY
(T.Y.B.SC.MATHEMATICS) SEM VI
CIE Backlog Examination, Mar-Apr 2024
(Sem-VI - Backlog)

Date	Time	Subject Code	Subject
03-04-2024	1.15 pm to 2.00 pm	19ScMatU601	Complex Analysis
04-04-2024	1.15 pm to 2.00 pm	19ScMatU602	Mathematical Analysis
05-04-2024	1.15 pm to 2.00 pm	19ScMatU603	Problem Solving course based on CCT-15 and CCT-16
06-04-2024	1.15 pm to 2.00 pm	19ScMatU604	Ring Theory
12-04-2024	1.15 pm to 2.00 pm	19ScMatU605	Number Theory
13-04-2024	1.15 pm to 2.00 pm	19ScMatU606	Problem Solving course based on CCT-18 and CCT-19
15-04-2024	1.15 pm to 2.00 pm	19ScMatU607/ 19ScMatU608	Lebesgue Integration/ Dynamical Systems
16-04-2024	1.15 pm to 2.00 pm	19ScMatU609/ 19ScMatU610	Financial Mathematics/ C Programming Language

Dr. R. M. Jagtap
Controller of Examinations

Dr. Anjali Sardesai
Coordinator, IQAC

Dr. R. S. Zunjarrao
Principal

Instructions to Students:

1. The examination hall for CIE Backlog Examination will be communicated to you by concerned Department.
2. For any query related to CIE Backlog Examination time-table, please contact concerned subject teacher /Department.
3. For CIE Backlog Examination, please ensure that you will reach the examination Hall 10 minutes before examination time.
4. Duly signed Hall ticket and college ID card is compulsory to attend the examinations.
5. Use of mobile phone and smart watch is strictly prohibited.
6. If any misconduct by any student will be confirmed by Examination Squad during examination, then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.