



## Progressive Education Society's

## Modern College of Arts, Science and Commerce (Autonomous)

Shivajinagar, Pune -5 Time Table for Third Year UG

SCIENCE FACULTY

(T.Y.B.SC.MATHEMATICS) SEM V

CIE Backlog Examination, Mar-Apr 2024

(Sem-V Backlog)

Date	Time	Subject Code	Subject
22-03-2024	1.15 pm to 2.00 pm	19ScMatU501	Metric Spaces
23-03-2024	1.15 pm to 2.00 pm	19ScMatU502	Real Analysis
26-03-2024	1.15 pm to 2.00 pm	19ScMatU503	Problem Solving course based on CCT-9 and CCT-10
27-03-2024	1.15 pm to 2.00 pm	19ScMatU504	Group Theory
28-03-2024	1.15 pm to 2.00 pm	19ScMatU505	Partial Differential Equations
30-03-2024	1.15 pm to 2.00 pm	19ScMatU506	Problem Solving course based on CCT-12 and CCT-13
01-04-2024	1.15 pm to 2.00 pm	19ScMatU507/ 19ScMatU508	Operations Research/ Graph Theory
02-04-2024	1.15 pm to 2.00 pm	19ScMatU509/	Laplace Transforms and Fourier Series/
		19ScMatU510	Computational Geometry

Dr. R. M. Jagtap Controller of Examinations

Dr. Anjali Sardesai Coordinator, IQAC

Dr. R. S. Zunjarrao Principal

## **Instructions to Students:**

- 1. The examination hall for CIE Backlog Examination will be communicated to you by concerned Department.
- 2. For any query related to CIE Backlog Examination time-table, please contact concerned subject teacher /Department.
- 3. For CIE Backlog Examination, please ensure that you will reach the examination Hall 10 minutes before examination time.
- **4.** Duly signed Hall ticket and college ID card is compulsory to attend the examinations.
- 5. Use of mobile phone and smart watch is strictly prohibited.
- 6. If any misconduct by any student will be confirmed by Examination Squad during examination, then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.