Progressive Education Society's

Modern College of Arts, Science and Commerce (*Autonomous*)

Shivajinagar, Pune -5

Time Table for Third Year UG
COMMERCE FACULTY
(T.Y.BBA CA) SEM V
CIE Backlog Examination, Mar-Apr 2024
(Sem-V Backlog)

Date	Time	Subject Code	Subject Name
28-03-2024	1.15 pm to 2.00 pm	19BaBbcU501	Java programming
30-03-2024	1.15 pm to 2.00 pm	19BaBbcU502	Web technology
01-04-2024	1.15 pm to 2.00 pm	19BaBbcU503	Information System security
02-04-2024	1.15 pm to 2.00 pm	19BaBbcU504	Software Project Management
03-04-2024	1.15 pm to 2.00 pm	19BaBbcU508	Artificial Intelligence

Dr. Neha Dixit
Dy. COE

Dr. R. M. Jagtap
COE/CEO

Dr.Anjali Sardesai
Coordinator, IQAC

Dr. R. S. Zunjarrao
Principal

Instructions to Students:
1. The examination hall for CIE Backlog Examination will be communicated to you by concerned Department.
2. For any query related to CIE Backlog Examination time-table, please contact concerned subject teacher /Department.
3. For CIE Backlog Examination, please ensure that you will reach the examination Hall 10 minutes before examination time.
4. Duly signed Hall ticket and college ID card is compulsory to attend the examinations.
5. Use of mobile phone and smart watch is strictly prohibited.
6. If any misconduct by any student will be confirmed by Examination Squad during examination, then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.