Only for Autonomous As prepared on: 05-03-2024



Progressive Education Society's Modern College of Arts, Science and Commerce (Autonomous) Shivajinagar, Pune -5 Time Table for Second Year UG S.Y.B B. A. (CA) Semester IV CIE Backlog Examination, Mar-Apr 2024 Sem IV Backlog

Date	Time	Subject Code	Subject Name
Regular Credits			
27-03-2024	4.45 pm to 5.30 pm	19BaBbcU401	Data Mining and Data Warehousing
28-03-2024	4.45 pm to 5.30 pm	19BaBbcU402	C#.NET
30-03-2024	4.45 pm to 5.30 pm	19BaBbcU403	Concepts of Operating system
01-04-2024	4.45 pm to 5.30 pm	19BaBbcU404	Object oriented Software Engineering
02-04-2024	4.45 pm to 5.30 pm	19BaBbcU405	Computer networks
03-04-2024	4.45 pm to 5.30 pm	19CpEngU402	English Communication-II



Dr. <u>Neha</u> Dixit Dy. COE



Dr. R. M. Jagtap COE/CEO



Dr.Anjali Sardesai Coordinator, IQAC

Dr. R. S. <u>Zunjarrao</u> Principal

Instructions to Students:

- 1. The examination hall for CIE Backlog Examination will be communicated to you by concerned Department.
- 2. For any query related to CIE Backlog Examination time-table, please contact concerned subject teacher /Department.
- 3. For CIE Backlog Examination, please ensure that you will reach the examination Hall 10 minutes before examination time.
- 4. Duly signed Hall ticket and college ID card is compulsory to attend the examinations.
- 5. Use of mobile phone and smart watch is strictly prohibited.
- 6. If any misconduct by any student will be confirmed by Examination Squad during examination, then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.