Only for Autonomous As prepared on: 04-03-2024



Progressive Education Society's Modern College of Arts, Science and Commerce (Autonomous) Shivajinagar, Pune -5 Time Table for Third Year UG SCIENCE FACULTY (T.Y.B.SC. ELECTRONIC SCIENCE) SEM V End Semester Examination, Mar-Apr 2024 (Sem-VI Regular & Backlog)

Date	Time	Subject Code	Subject
MOOC Based/Extra Credits			
08-04-2024	11.00 am to 12 noon	19CpArtU602	Fundamental of Music-II (50 M, 02 Credits) (50M)
10-04-2024	11.00 am to 12 noon	20CpPolU201	Democracy , Election and Governance (02 Credit, 50 M) Only for TY UG 2021-2022 Backlog students
Regular Credits			
01-04-2024	11.00 am to 1.00 pm	19ScEleU601	Electronic Instrumentation and Robotics
02-04-2024	11.00 am to 1.00 pm	19ScEleU602	Advanced Microcontroller : PIC
03-04-2024	11.00 am to 1.00 pm	19ScEleU603	Industrial Power Electronics
04-04-2024	11.00 am to 1.00 pm	19ScEleU604	Modeling and Simulation of System using MATLAB
05-04-2024	11.00 am to 1.00 pm	19ScEleU605	Advanced Electronic Communication and IoT
06-04-2024	11.00 am to 1.00 pm	19ScEleU607	Fiber Optic Communication

Dr. R. M. Jagtap Controller of Examinations

Dr. Anjali Sardesai Coordinator, IQAC

Dr. R. S. Zunjarrao Principal

## **Instructions to Students:**

- 1. Please ensure that you will reach the examination Hall 25 minutes before examination time.
- 2. Duly signed Hall ticket and college ID card is compulsory to attend the examinations
- 3. Use of mobile phone and smart watch is strictly prohibited.
- 4. If any misconduct by any student will be confirmed by Examination Squad during examination then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.
- 5. A separate time-table is displayed for Regular Credit CIE Backlogs.