




Only for  
Autonomous  
As prepared on:  
04-03-2024

*Progressive Education Society's*  
**Modern College of Arts, Science and Commerce (Autonomous)**  
Shivajinagar, Pune -5  
**Time Table for Third Year UG**  
**SCIENCE FACULTY**  
**(T.Y.B.SC. ELECTRONIC SCIENCE) SEM V**  
**End Semester Examination, Mar-Apr 2024**  
**(Sem-V Backlog)**

Date	Time	Subject Code	Subject
<b>MOOC Based/Extra Credits</b>			
19-03-2024	11.00 am to 12 noon	19CpPedU101^	Physical Education I (01 Credit, 25 M) (Only for TY UG 2021-2022 Backlog students)
20-03-2024	11.00 am to 12 noon	19CpArtU502	Fundamental of Music-I (50 M, 02 Credits) (50M)
<b>Regular Credits</b>			
22-03-2024	11.00 am to 1.00 pm	19ScEleU501	Analog Integrated Circuits and applications
23-03-2024	11.00 am to 1.00 pm	19ScEleU502	8051 Microcontroller interfacing and Applications
26-03-2024	11.00 am to 1.00 pm	19ScEleU503	Digital System Design with Verilog
27-03-2024	11.00 am to 1.00 pm	19ScEleU504	Principles of Semiconductor Devices
28-03-2024	11.00 am to 1.00 pm	19ScEleU505	Fundamentals of Electronic Communication
30-03-2024	11.00 am to 1.00 pm	19ScEleU506	"C" Programming

  
Dr. R. M. Jagtap  
Controller of Examinations

  
Dr. Anjali Sardesai  
Coordinator, IQAC

  
Dr. R. S. Zunjarrao  
Principal

**Instructions to Students:**

1. Please ensure that you will reach the examination Hall 25 minutes before examination time.
2. Duly signed Hall ticket and college ID card is compulsory to attend the examinations
3. Use of mobile phone and smart watch is strictly prohibited.
4. If any misconduct by any student will be confirmed by Examination Squad during examination then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.
5. A separate time-table is displayed for Regular Credit CIE Backlogs.