



Only for Autonomous
As prepared on: 05-03-2024

Progressive Education Society's
Modern College of Arts, Science and Commerce (Autonomous)
Shivajinagar, Pune -5
Time Table for First Year UG
F.Y. B. SC COMP. SCIENCE
CIE Backlog Examination, Mar-Apr 2024
(Sem-II Backlog)

Date	Time	Paper Code	Subject
Regular Credits			
02-04-2024	1.15 pm to 2.00 pm	19CsCmpU201	Advanced C Programming
03-04-2024	1.15 pm to 2.00 pm	19CsCmpU202	Relational Database Systems
05-04-2024	1.15 pm to 2.00 pm	19CsEleU201	Fundamentals of Electronic instrumentation
06-04-2024	1.15 pm to 2.00 pm	19CsEleU202	Fundamentals of Digital Electronic Circuits
08-04-2024	1.15 pm to 2.00 pm	19CsMatU201	Graph Theory
10-04-2024	1.15 pm to 2.00 pm	19CsMatU202	Calculus
12-04-2024	1.15 pm to 2.00 pm	19CsStaU201	Applied Statistics and Queuing Models
13-04-2024	1.15 pm to 2.00 pm	19CsStaU202	Continuous Probability Distributions and Testing of hypotheses
15-04-2024	1.15 pm to 2.00 pm	19CsStaU203	Statistics Practical-II

Dr. Yugandhar Shinde
Dy. COE

Dr. R. M. Jagtap
CoE

Dr. Anjali Sardesai
Coordinator, IQAC

Dr. R. S. Zunjarrao
Principal

Instructions to Students:

1. The examination hall for CIE Backlog Examination will be communicated to you by concerned Department.
2. For any query related to CIE Backlog Examination time-table, please contact concerned subject teacher /Department.
3. For CIE Backlog Examination, please ensure that you will reach the examination Hall 10 minutes before examination time.
4. Duly signed Hall ticket and college ID card is compulsory to attend the examinations.
5. Use of mobile phone and smart watch is strictly prohibited.
6. If any misconduct by any student will be confirmed by Examination Squad during examination, then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.