Progressive Education Society's
Modern College of Arts, Science and Commerce (*Autonomous*)
Shivajinagar, Pune -5
Time Table for First Year B.Sc. (Computer Science)
CIE Backlog Examination, Mar/Apr 2024
(Sem- I Backlog)

Date	Time	Paper Code	Subject
Regular Credits			
20-03-2024	1.15 pm to 2.00 pm	19CsCmpU101	Problem Solving and Introduction to C Programming
21-03-2024	1.15 pm to 2.00 pm	19CsCmpU102	Introduction to Scripting Languages (HTML, CSS)
22-03-2024	1.15 pm to 2.00 pm	19CsEleU101	Basics of Electronic Components and Circuits
23-03-2024	1.15 pm to 2.00 pm	19CsEleU102	Fundamentals of Digital Electronics
26-03-2024	1.15 pm to 2.00 pm	19CsMatU101	Discrete Mathematics
27-03-2024	1.15 pm to 2.00 pm	19CsMatU102	Algebra
28-03-2024	1.15 pm to 2.00 pm	19CSStaU101	Descriptive Statistics
30-03-2024	1.15 pm to 2.00 pm	19CSStaU102	Probability theory and Discrete probability distributions
01-04-2024	1.15 pm to 2.00 pm	19CsStaU103	Statistics Practical - I

Dr. Yugandhar Shinde
Dy. COE

Dr. R. M. Jagtap
CoE

Dr. Anjali Sardesai
Coordinator, IQAC

Dr. R. S. Zunjarrao
Principal

Instructions to Students:
1. The examination hall for CIE Backlog Examination will be communicated to you by concerned Department.
2. For any query related to CIE Backlog Examination time-table, please contact concerned subject teacher /Department.
3. For CIE Backlog Examination, please ensure that you will reach the examination Hall 10 minutes before examination time.
4. Duly signed Hall ticket and college ID card is compulsory to attend the examinations.
5. Use of mobile phone and smart watch is strictly prohibited.
6. If any misconduct by any student will be confirmed by Examination Squad during examination, then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.