

Only for Autonomous
As prepared on: 15-03-2023



Progressive Education Society's
Modern College of Arts, Science and Commerce (Autonomous)
Shivajinagar, Pune -5
Time Table for First Year PG
SCIENCE FACULTY M.Sc. Electronic Science
ESE Examination Apr-May 2023
Sem – I Backlog

Date	Time	Paper Code	Subject
10-05-2023	11.30 am to 1.30 pm	19ScEleP101	Analog Circuit Design and Analysis
11-05-2023	11.30 am to 1.30 pm	19ScEleP102	Mathematical Methods and Network analysis.
12-05-2023	11.30 am to 1.30 pm	19ScEleP103	Foundation of Semiconductor Devices and Circuits
13-05-2023	11.30 am to 1.30 pm	19ScEleP104	Advanced 'C' Programming

Dr. Yugandhar Shinde
Dy. COE

Dr. R. M. Jagtap
CoE

Dr. Anjali Sardesai
Coordinator, IQAC

Dr. R. S. Zunjarrao
Principal

Instructions to Students:

1. For '**Practical Examinations**' contact your Department.
2. Please ensure that you have reached the examination Hall 25 minutes before examination time.
3. Duly signed Hall ticket and college ID card is compulsory to attend the examinations
4. Use of mobile phones/smart watch/ear phone is strictly prohibited.
5. If any misconduct by any student will be confirmed by Examination Squad during examination then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.
6. For CIE/Extra credit Backlogs, Please contact your Department.
7. For Extra credit's separate time-table will be displayed. Your PG degree will not be completed if you will not clear your extra credits.