



Progressive Education Society's

Modern College of Arts, Science and Commerce (Autonomous)

Shivajinagar, Pune -5

Time Table for Second Year PG

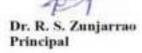
SCIENCE FACULTY (M.Sc. Electronics) ESE Examination Apr-May 2023

Sem – III Backlog

Date	Time	Subject Code	Subject Name
02-05-2023	11.30 am to 1.30 pm	19ScEleP301	PIC Microcontroller
03-05-2023	11.30 am to 1.30 pm		ARM and RTOS/ Digital Signal Processing
04-05-2023	11.30 am to 1.30 pm	19ScEleP306/	Digital Image and Video Processing/ Electronics and Computer Architecture







Instructions to Students:

- 1. Please ensure that you have reached the examination Hall 25 minutes before examination time.
- 2. Duly signed Hall ticket and college ID card is compulsory to attend the examinations
- 3. Use of mobile phones/smart watch is strictly prohibited.
- 4. If any misconduct by any student will be confirmed by Examination Squad during examination then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.
- 5. For CIE/Extra credit Backlogs, Please contact your Department.
- 6. Your PG degree will not be completed if you will not clear your extra credits.