



Progressive Education Society's
Modern College of Arts, Science and Commerce (Autonomous)
Shivajinagar, Pune -5
Time Table for Third Year UG
SCIENCE FACULTY
(T.Y.B.SC. ELECTRONIC SCIENCE) SEM V
End Semester Examination, Mar-Apr 2023
(Sem-VI Regular & Backlog)

Date	Time	Subject Code	Subject
20/03/2023	11.30 am to 1.30 pm	19ScEleU601	Electronic Instrumentation and Robotics
21/03/2023	11.30 am to 1.30 pm	19ScEleU602	Advanced Microcontroller : PIC
23/03/2023	11.30 am to 1.30 pm	19ScEleU603	Industrial Power Electronics
24/03/2023	11.30 am to 1.30 pm	19ScEleU604	Modeling and Simulation of System using MATLAB
25/03/2023	11.30 am to 1.30 pm	19ScEleU605	Advanced Electronic Communication and IoT
27/03/2023	11.30 am to 1.30 pm	19ScEleU607	Fiber Optic Communication

Dr. R. M. Jagtap
Controller of Examinations

Dr. Anjali Sardesai
Coordinator, IQAC

Dr. R. S. Zunjarrao
Principal

Instructions to Students:

- 1. For CIE and Extra Credits Backlog examinations separate time tables will be displayed on college website.**
- 2. Please ensure that you have reached the examination Hall 25 minutes before examination time.**
- 3. Duly signed Hall ticket and college ID card is compulsory to attend the examinations**
- 4. Use of mobile phone and smart watch is strictly prohibited.**
- 5. If any misconduct by any student will be confirmed by Examination Squad during examination then appropriate Disciplinary action will be admissible against the student as per SPPU rules.**