



Only for Autonomous

As prepared on:

25.02.2023

Progressive Education Society's
Modern College of Arts, Science and Commerce (Autonomous)
Shivajinagar, Pune -5
Time Table for Third Year UG
SCIENCE FACULTY
(T.Y.B.SC. ELECTRONIC SCIENCE) SEM V
End Semester Examination, Mar-Apr 2023
(Sem-V Backlog)

Date	Time	Subject Code	Subject
13/03/2023	11.30 am to 1.30 pm	19ScEleU501	Analog Integrated Circuits and applications
14/03/2023	11.30 am to 1.30 pm	19ScEleU502	8051 Microcontroller interfacing and Applications
15/03/2023	11.30 am to 1.30 pm	19ScEleU503	Digital System Design with Verilog
16/03/2023	11.30 am to 1.30 pm	19ScEleU504	Principles of Semiconductor Devices
17/03/2023	11.30 am to 1.30 pm	19ScEleU505	Fundamentals of Electronic Communication
18/03/2023	11.30 am to 1.30 pm	19ScEleU506	"C" Programming

Dr. R. M. Jagtap
Controller of Examinations

Dr. Anjali Sardesai
Coordinator, IQAC

Dr. R. S. Zunjarrao
Principal

Instructions to Students:

- 1. For CIE and Extra Credit Backlog examinations separate time-tables will be displayed on college website.**
- 2. Please ensure that you have reached the examination Hall 25 minutes before examination time.**
- 3. Duly signed Hall ticket and college ID card is compulsory to attend the examinations**
- 4. Use of mobile phone and smart watch is strictly prohibited.**
- 5. If any misconduct by any student will be confirmed by Examination Squad during examination then appropriate 'Disciplinary Action' will be admissible against the student as per SPPU rules.**